# Temperature Control on FPGA using SPI Protocol with Thermopar Max6675k

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*Abstract* - This paper shows the design of a temperature control for a water heater using a digital controller on an FPGA with SPI communications protocol, which makes use of a thermo-resistance with alternating current (110v-60Hz) to heat water from the room temperature to its boiling point. The Dimmer that has a Triac BTA16-600B works with control values between 3.3-5 Volts with a charge value for up to 220 Volts AC and peak currents of 5 Amps, the temperature sensor is a K-type thermocouple coupled to a Cold junction Max 6675K compensator.

Keyword-FPGA, Digital Control, Temperature control, Measuring and instrumentation.

## I. INTRODUCTION

Currently the work of electronic instrumentation has changed radically, since the integration of signal conditioning circuits [1], make it not only required to perform the digital readings by a communication bus [2], but also to design robust systems that allow centralizing information through a field bus between different programmable digital devices and thus have control of all system variables. [3-4]

At present, there is a strong tendency to use programmable logic devices type FPGA to perform the calculations of various digital controllers [5], since they can perform the necessary calculations concurrently; what makes them ideal in applications that require a high response speed [6], dynamic shift of the sampling period [7], in addition to performing tasks of communication and visualization of the system variables in a parallel way in a graphical interface [8].

The systems of data acquisition and temperature variable control are widely used in various industrial and productive applications, ranging from a simple temperature control of a tank, machines for precision metal welding [9], chemical plants [10], until the temperature control of one of the modules of a spaceship [3]. In general terms, you can find a large number of applications that carry out processing tasks with fairly short sampling and capture times [11], making corrections to drift errors, errors due to Gaussian noise, delays due to acquisition time, correction by cold juncture, etcetera. [12]; procedures that were performed with analog circuits, but nowadays are performed with sensors packed in the same silicon module that performs the work of conditioning and transmission of information.

In addition to the process of signal acquisition and controller calculation, linearization and signal filtering tasks are required [13-14], along with the possibility of performing control systems such as PID, Fuzzy Logic [15] or another digital technique that fits the needs of the system, even could be used auto-tuning techniques applicable in an FPGA-type device to some of the aforementioned control techniques.

## **II. METHODOLOGY**

#### A. Cold junction compensator Max 6675K

The operations of a thermocouple are based on the energy change in which there is a flow of energy from the area of higher temperature to lower temperature, in the union of the two metals that this thermocouple is formed a potential difference between the ends which make up the metals and varies depending on the difference in temperatures. The responses of these measurements depends on the nature of these metals and their manufacture, the part that comes into contact with the element to be measured is called hot junction which is directly the measurement that is of interest for the operation of the system. The tips of the elements where the measurement is taken electrically are called cold junction. The equation that describes this electrical behavior is given by:

$$Vtermopar = \alpha * (T^{\alpha} - To) = \alpha * T^{\alpha} - \alpha * To$$
(1)

Where  $\alpha$  is a parameter dependent on the types of materials used for its manufacture and gives the sensitivity of the tension at the exit of the cold junction, To is the ambient temperature where the measurement will be made and  $T^{\alpha}$  the temperature of interest. [16]

When the measured values are acquired, the values of the thermocouple (hot junction) and the ambient value (cold junction) must be taken into account, however with the variation of the ambient temperature it is logical to think that the temperature value or the voltage output change. With the cold junction compensator, in this case, the use of the Max 6675K module, its main function is the elimination of the ambient temperature, this is done by adding a cancellation variable to the equation that describes the measurement behavior, this term is,  $\alpha *$  To physically create an auxiliary circuit that depends on the same ambient temperature to operate and delivers a cancellation voltage to that created by the cold junction, see Fig. 1.



Fig1. Basic cold junction compensation circuit [17]

The main element of the compensating circuit is a thermistor dependent on the ambient temperature, based on a semiconductor that varies its response depending on the temperature, the output voltage after the rectification is expressed as:

$$termistor = \beta * To \qquad (2$$

)

Where  $\beta$  is precisely the sensitivity of the circuit and the equation of the voltage divider is given by:

V

$$V compensador = VR1 = V termistor * \frac{R1}{R1 + R2} = \beta * To * \frac{R1}{R1 + R2}$$
(3)

The compensation is as simple as getting a voltage value with the Vtermopar quantity to cancel the effect of the cold junction, in the meanwhile:

$$Vtermopar = \alpha * T^{\alpha} - \alpha * To * \frac{R1}{R1 + R2}$$
(4)

The condition to fulfill is:

$$\beta * To * \frac{R1}{R1 + R2} = \alpha To \qquad (5)$$

The cold junction compensator Max 6675k is in its module a digital analog converter specialized to work with type k thermocouples, which consists of the NiCr-Ni or NiCr-NiAl material combinations; this thermocouple in working with the cold junction compensator Max 6675K has a measurement range from 0° to 1024° Celsius with a resolution of 12 bits, 0.25° Celsius. The SPI communication protocol uses the 12 bits for unidirectional data communication and discards mainly the most significant bit, since it only works in the range of positive values. The temperature conversion times range between 170 ms and 220 ms, in order to have a clear and concise measurement with its measurement time value it is recommended to use a longer time than the upper one, in this case it is recommended to request the sensor data 250 ms between measurement and measurement.



Fig 2. Max6675K with thermo pair type K

#### B. SPI Protocol (Serial Peripheral Interface)

The SPI serial communications protocol was born in the early 1980s with the effort of Motorola as one of the most popular work protocols for its speed of transmission, simplicity and easy operation with many devices, its application can range from screens to microcontrollers.

The synchronous protocol works in full duplex mode to transmit and receive information, being able to communicate bi-directionally and unidirectionally with the devices that support this type of communication through a direct line so that the entire process is synchronous.



Figure 3. Basic Master-Slave structure

The protocol always defines a master (Master) which is responsible for transmitting and receiving information to its connected elements (slaves), it should be noted that for the Master to receive information a second shift register is necessary, one assigned to the Master and one for the Slave, this is because the storage of bits is clearly done in parallel to then a serial conversion and thus the arrival of information.

The 4 types of logical lines are, MOSI (Master out salve in), MISO (Master in slave out), CLK and SS. The first two are used for master-slave and slave-master communication. CLK is the synchronous line that comes from the master and is used to precisely synchronize the sending and receiving of data between the elements of the system. And finally the SS line when working with more than one slave is responsible for the selection of the slave in the system.

# **III. IMPLEMENTATION AND RESULTS**

## Reading the MAX6675 Sensor using the FPGA

Fig. 4 shows some sections of the Max6675 sensor reading using the SPI protocol through the FPGA, in the first instance a driver was made to read the data and display them in a seven segment display, using a Binary to BCD converter generalized for N-bits.



To perform the reading of the sensor, a finite state machine FSM was designed and implemented, see Fig. 5 (a), which performs the management of the SPI protocol together with the control of the sampling frequency of the signal acquisition system of temperature, see Fig. 5(b).



b)

Figure 5. (a) State diagram (b) Diagram of blocks

Modeling, design and implementation of digital control over the FPGA



Figure 6. Loop Diagram closed 1 and 2

The closed loop system in this case can be described as a feedback system with gain 1, since the sensor directly delivers the measured values and it is not necessary to convert and manipulate the data delivered by it.

The data delivered by the FPGA, which were subsequently transmitted under the SPI communication, are analyzed and validated by the system under a PID controller, are delivered to the actuator under a temperature value set by the user. The PID controller under a compensation coefficient and with a peak over no more than 5% guarantees a heating speed in the shortest possible time; it is a system that guarantees a non-saturation of the Dimmer.

The response in open loop was found with the measurement of heating times until these measurements reached desired constant values. The Matlab identifier gives us a graph of temperature vs. time, see Fig. 7, in which it can be seen that the maximum stable value is around 92 degrees, which is the boiling value of water at the height of the city in the that the tests were done.



Figure 7. Matlab and Excel open-loop response

Obtaining the data gives us a growth value of 240 seconds before reaching a stable value; the blue line represents the response obtained with real values measured with a thermocouple and a computational timer; under it, the response obtained with coefficients that represent this behavior mathematically is found in orange. The tool "rltool" gives us a mathematical representation of poles, zeros and in this case the limit of the compensator value for a peak envelope of no more than 5%, in addition to a view of the compensation values and the attempt of an equation of transfer function between the input in AC voltage values and the output in terms of temperature. The poles and zeros of the PID controller are shown below.



Figure 8. Root Locus, Poles, Zeros in PID

In Fig. 8 we can see the V shape where the maximum no-peak envelope of 5% is represented, limiting the value of the compensator and the regions where we can define our zeros and therefore the region for the aforementioned compensator coefficient. The type of control used was a PID controller. During his choice, this type of control was defined due to its low saturation in terms of compensation value at the Dimmer output.

Architecture	Compensato	or Editor Gr	aphical Tuning	Analysis Plots	Automated Tuning	
Compensator C = 0.035377 x $\frac{(1 + 1.7e+02s + (1e+02s)^2)}{s}$ Pole/Zero Parameter						
Dynamics Edit Selected Dynamics						
Туре	Location	Damping	Frequency			
Integrator	0	-1	0			
Right-click	to add or del	ete poles/zer	05	Loca	tion 0	
Show Architecture Store Design Help						
(a)						



(b)

Figure 9. (a) Controller coefficients (b) control response

The compensation coefficient is observed along with its characteristic equation and below the coefficients that describe the behavior. In Fig. 9 it can be seen a stabilization time of 700 seconds and a maximum rise point of 200 seconds, with a rise peak of no more than 5%. In the export of the controller and then with the conversion to a discrete equation it is obtained the following equation.

>> c2d(C,0.5,'tustin')
ans =
 1445.3 (z^2 - 1.992z + 0.9919)
 ....
 (z-1) (z+1)
Sample time: 0.5 seconds
Discrete-time zero/pole/gain model.

#### Figure 10. Controller discretized equation

For the conversion to a discrete equation, the frequency was declared at 2Hz since the refresh value at the output of the cold junction compensator is 4Hz, thus guaranteeing a new value whenever a controller data is obtained. The equation in terms of P(n) is:

$$C(z) = \frac{1445.3(z^2 - 1.992z + 0.9919)}{(z - 1)(z + 1)} = \frac{P(z)}{T(z)}$$
(6)

$$\frac{z^{-2}}{z^{-2}} * \frac{1445.3(z^2 - 1.992z + 0.9919)}{(z - 1)(z + 1)} = \frac{P(z)}{T(z)}$$
(7)

$$\frac{1445.3(1-1.992z^{-1}+0.9919z^{-2})}{1-z^{-2}} = \frac{P(z)}{T(z)}$$
(8)

$$1445.3T(z) - 2879.0376T(z)z^{-1} + 1433.59307T(z)z^{-2} = P(z) - P(z)z^{-2}$$
(9)

$$1445.3T(n) - 2879.0376T(n-1) + 1433.59307T(n-2) = P(n) - P(n-2)$$
(10)

$$P(n-2) + 1445.3T(n) - 2879.0376T(n-1) + 1433.59307T(n-2) = P(n)$$
(11)

The discrete operation and the behavior of the measurement system together provide a simple measurement when performing work in environments with little time to work. The variable configuration of the desired temperature and the maximum peak envelope increase its versatility to work in ranges from 0° to 1024° Celsius, which are allowed by the cold junction compensator and type K thermocouple.

# **IV. CONCLUSIONS**

The implementation of a PID controller was made, seeing the behavior of the different types of P, PD and PID controllers, a controller with a low compensation value is obtained that does not saturate the output of the PWM to control the Dimmer of the system. This control allows defining the peak overheating, which makes it one of the most dynamic controls to work in this type of systems with slow response.

For the implementation of the reading of the sensor and the digital controller, less than 4% of the total resource of a Xilinx Spartan 3E FPGA of one hundred thousand gates was used, a state machine was made to perform the digital readings, using only 2% of the sequential and combinatorial digital resources available for this low cost FPGA; In addition to this, the maximum delay time of both the controller and the driver that reads the sensor data is only 2.45 nanoseconds, which means that the sampling time of the controller could be up to 408 Mhz, result that would allow drivers with higher sampling times or multi-frequency techniques or simply to make a single controller with so little resource, that could perform up to 20 controls in parallel of different processes that require temperature measurement without any inconvenience.

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