

# Low Noise Figure and High Gain Single Stage Cascoded LNA Amplifier With Optimized Inductive Drain Feedback for WiMAX Application

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**Abstract**— This project presents a low noise and high gain cascoded LNA amplifier for direct conversion RF front end receiver architecture, which operates at 5.8 GHz unlicensed band. The LNA used Transistor FHX76LP superHEMT low noise FET. This LNA was designed and used inductive feedback and T- matching network consisting of lump reactive at the input and output of the LNA circuit. A single cascoded LNA design was built to meet the standard of IEEE 802.16. The cascoded LNA produced low noise figure of 0.83 dB with high gain of 26.26 dB. The S-parameter for the input reflection  $S_{11}$ , output reflection  $S_{22}$ , and return loss  $S_{12}$  are of -11.05 dB, -10.5 dB and -30.92 dB respectively. The bandwidth measured is 1.56 GHz, while the input sensitivity is -82.6 dBm which is compliant WiMAX standards. The single stage LNA amplifier simulated using Ansoft Designer SV.

**Keywords**- RF front-end; Direct Conversion; WiMAX; Receiver Sensitivity; IEEE 802.16; Cascoded LNA; inductive feedback.

## 1. INTRODUCTION

Radio-frequency (RF) transmission is one of the best examples to show a very challenging technological demands in communication systems. Systems built using RF transmission often used in daily life such as mobile phones, notebooks, wireless sensors, among other, require an increasing versatility and suggest an ability to storage of data transmission rates of a huge information and size reduction. In this paper, we want to discuss, the solutions needed to be done to address the increasing number of wireless personal communication systems demand for Radio Frequency (RF) front-end receivers capable to handle standard specification difference, i.e. WiMAX, WLAN, WiFi. According to the standard IEEE 802.16 (WiMAX) it can transmit data rates exceeding speeds of 70 Mbps and a service area of about 50km for fixed stations and 5-15 km for mobile stations [2].

For a RF front-end receiver used in WiMAX system at 5.8 GHz would have to be designed for desired frequency, gain, bandwidths and noise figure. Multiple parameters such as gain and noise figure in the RF component for the front – end receiver would have to be compensated. These trade-offs are the challenges that RF designers have to consider in designing a high performance communication system.

In Fig. 1 it shows a direct conversion RF front-end receiver. It consists of an antenna, cascaded LNA and a filter. The RF input signal fed through the system is very weak. Since the RF front-end requires amplifiers to amplify the RF signal and at the same time lowering the noise figure that passes through it, this can only be done by using an LNA. To design a low noise amplifier of the RF front-end receiver, we will be face many drawbacks. Moreover, the circuit must meet certain specifications as well as to provide suitable input impedance match, sufficient gain power and low noise figure (NF) within the required band [4].

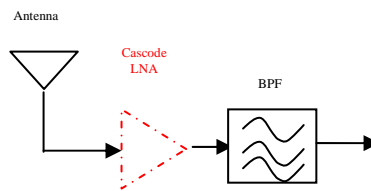


Fig 1. Direct conversion RF front end receiver configuration for Wimax point to point communication at 5.8 GHz band

There are two types of amplifier topology used for LNA, it is cascading and cascoding technique. The cascoded topology is much popular since this technique can introduce higher gain, due to the increase in the output impedance, as well as better isolation between the input and output ports [9]. While cascaded topology used to increase the gain of the amplifier. To solve the problems of obtaining high gain and low noise encountered in RF receiver, we propose a single cascoded LNA amplifier using inductive feedback from gate to drain of an PHEMT for use as a sub component in the RF front end architecture.

Various methods and steps are used to design the amplifier and such design step must be in accordance with the IEEE 802.16 standard. The specification for the design of single cascoded LNA amplifier as shown in Table 1.

Table 1. Design specifications single cascoded LNA for direct conversion RF front end WiMAX receiver.

Parameter	Gain dB	Frequency ( GHz)	NF dB	Matching Technique	Bandwidth MHz	Input sensitivity dBm
Single Cascoded LNA	> 20	5.8	< 3	Microstrip and lump reactive element	>1000 (5.8 GHz Centre)	< -80

With reference to Table 1, the target gain is 20 dB or above. However, from our literature review for a single stage LNA most amplifiers reported using cascoded topology is less than 20 dB [4]. Most of the LNA amplifier reviewed have a noise figure of less than 3 dB and the bandwidth presented are more than 1GHz. In addition, the input sensitivity is less than -80dBm as compliant with the IEEE 802.16 standard.

## II. LNA THEORITICAL DESCRIPTION

The general topology of the LNA amplifier consists of three stages: the input matching circuit (IMC), the amplifier itself and the output matching circuit (OMC) [6], [7], [8]. Essentially, for a LNA amplifier design, we need to ensure that input and output matching network must meet the criteria required for stability, small signal gain and bandwidth [8]. The formula and mathematical statements used to design LNA amplifiers are obtained from Pozar [3]. Fig. 2 shows a typical single-stage LNA amplifier including input/output matching networks.

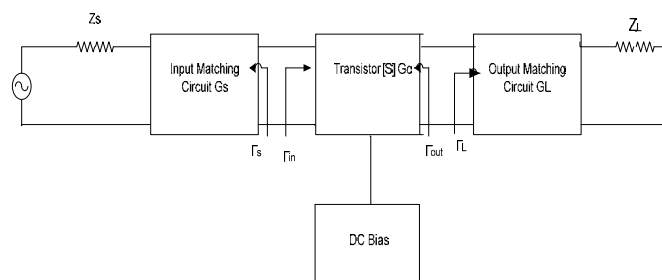


Fig. 2 Typical single-stage LNA amplifier

For a high frequency RF amplifiers, every designer have to ensure that the input and output matching networks for the amplifiers is matched to 50 ohms load at the input and output terminal [10]. I/O matching circuit is necessary to reduce unwanted reflection of signal and to improve the capability of transmission from source to load. The targeted S-parameter specification for a cascoded LNA amplifier is shown in Table 2.

Table 2. Targeted S-Parameters for LNA

S-parameter	Input reflection $S_{11}$ dB	Return Loss $S_{12}$ dB	Forward Transfer $S_{21}$ dB	Output Reflection loss $S_{22}$ dB	Noise Figure NF dB	Stability (K)
Single Cascoded LNA	< -10	< -20	>+ 20	<-10	< 3	$K > 1$

**2.1 Power Gain**

Essentially for the LNA amplifier to operate, the number of power gains derived from the output of the LNA amplifiers need to be considered. With reference to Fig. 3 for a 2 port power gain with power network circuit impedance or load impedance at the LNA amplifier are represented by scattering coefficients classified into Operating Power Gain, Power Transducer and Available Power Gain [3].

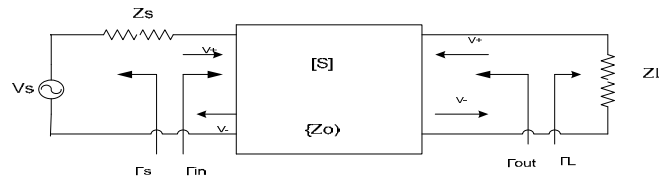


Fig. 3. I/O circuit of 2-port network [5]

Operating power gain is the ratio of the power dissipated in the load  $Z_L$  ( $P_L$ ) to the power delivered to the input ( $P_{in}$ ) of the two-port network [3]. The Operating Power Gain can be expressed as [13] :

$$G_P = \frac{P_L}{P_{in}} = \frac{|S_{21}|^2 (1 - |\Gamma_L|^2)}{(1 - |\Gamma_{in}|^2) |1 - S_{22}\Gamma_L|^2} \quad (1)$$

Where,  $\Gamma_{in}$  indicates reflection coefficient of load at the input port of 2-port network and  $\Gamma_s$  is reflection coefficient of power supplied to the input port.

Transducer Power Gain is the ratio of  $P_{avs}$ , maximum power available from source to  $P_L$ , power delivered to the load. Transducer Power Gain can be expressed by [14] :

$$G_P = \frac{P_L}{P_{in}} = \frac{|S_{21}|^2 (1 - |\Gamma_s|^2)(1 - |\Gamma_L|^2)}{|(1 - S_{11}\Gamma_s)(1 - S_{22}\Gamma_L) - (S_{12}S_{21}\Gamma_s\Gamma_L)|^2} \quad (2)$$

Where,  $\Gamma_L$  indicates load reflection coefficient.

Available Power Gain,  $G_A$  is the ratio of  $P_{avs}$ , power available from the source, to  $P_{avn}$ , power available from 2-port network, that is,  $G_A = \frac{P_{avn}}{P_{avs}}$ . Power gain is  $P_{avn}$  when  $\Gamma_{in} = \Gamma_s^*$ . Therefore Available Power Gain is given by [14] :

$$G_A = \frac{P_{avn}}{P_{avs}} = \frac{1 - |\Gamma_s|^2}{|1 - S_{11}\Gamma_s|^2} |S_{21}|^2 \frac{1}{|1 - S_{22}\Gamma_L|^2} \quad (3)$$

That is, the above formula indicates power gain when input and output are matched.

**2.1 Noise Figure**

Apart from the gain another design consideration must be taken into account in the design of LNA amplifiers that is noise figure. LNA amplifier is usually placed as the first stage of an RF front end receiver architecture. This provides the dominant effect on the overall system noise performance [3]. However, it is impossible to get an amplifier with minimal low noise figure and maximum gain; therefore, there should be a compromise between

these two parameters. It can be done by using constant gain circles and circles of constant noise figure to select usable trade-off between noise figure and gain. Typically, noise figure of 2-port transistor has a minimum value at the specified admittance given by [15] :

$$F = F_{\min} + \frac{R_N}{G_s} |Y_s - Y_{opt}|^2 \quad (6)$$

For low noise transistors, manufactures usually provide  $F_{\min}, R_N, Y_{opt}$  by frequencies.  $N$  defined by formula for desired noise figure:

$$N = \frac{|\Gamma_s - \Gamma_{opt}|^2}{1 - |\Gamma_s|^2} = \frac{F - F_{\min}}{4R_N / Z_0} |1 + \Gamma_{opt}|^2 \quad (7)$$

After stability of active device is determined, input and output matching circuits should be designed so that reflection coefficient of each port can be correlated with conjugate complex number as given below:

$$\Gamma_{IN} = \Gamma_s^* = S_{11} + \frac{S_{12}S_{21}\Gamma_L}{1 - S_{22}\Gamma_L} \quad (8)$$

And

$$\Gamma_{out} = \Gamma_L^* = S_{22} + \frac{S_{12}S_{21}\Gamma_s}{1 - S_{11}\Gamma_s} \quad (9)$$

To obtain minimum noise figure using 2-port transistor, source reflection coefficient should match with  $\Gamma_{opt}$  and load reflection coefficient should match with  $\Gamma_{out}^*$  with a complex conjugate number as formula below:

$$\Gamma_s = \Gamma_{opt} \quad (10)$$

$$\Gamma_L = \Gamma_{out}^* = \left( \frac{S_{22} + S_{12}S_{21}\Gamma_s}{1 - S_{11}\Gamma_s} \right) \quad (11)$$

### III. DESIGN OF CASCODED LNA

Cascoded low noise amplifier design is based on the specifications mentioned in the previous section. Types of transistors used in cascoded LNA are PHEMT FHX76LP. S-parameters for PHEMT is shown in Table 3, where the parameters are obtained at VDD = 2V and IDS = 10mA of biasing set at PHEMT.

Table 3. S-parameter from Transistor PHEMT FHX76LP datasheet

Frequency GHz	S <sub>11</sub>	S <sub>12</sub>	S <sub>21</sub>	S <sub>22</sub>
5.8 GHz	0.712	0.065	8.994	0.237

In determining the overall performance of the low noise amplifier we need to obtain the value of transducer gain ( $G_T$ ), noise figure (F) and the input and output standing wave ratios,  $VSWR_{IN}$  and  $VSWR_{OUT}$  either through calculations or simulations. The optimum,  $\Gamma_{opt}$  and  $\Gamma_L$  were obtained as  $\Gamma_{opt} = 17.354 + j 50.131$  and  $\Gamma_L = 79.913 - j7.304$  respectively. T-matching network was used in the input and output impedance. Lump reactive elements and microstrip line impedance are used to design the element of the T-network. By using Ansoft Designer SV Smith Chart matching technique, the desired component is shown in Table 4.

Table 4. Single- Stage Cascoded LNA Amplifier parameters

Components	Value
L1	1.16 nH
L2	1.35nH
L3	0.71nH
L4	0.40nH
L5	0.92NH
L6	6.5nH
L7	1.38nH
L8	0.76nH
L9	1.27nH
L10	0.07NH
CA	0.5pF
CB	7.5pF

The design of cascoded LNA has its own topology, where there is an inductive feedback L6 is connected to the drain of the M1, inductive source generation L10, which is connected to the source M2, while inductive L5 placed between the source and drain of M1 in M2 refer to Fig. 4. The cascoded LNA amplifier circuit designed using inductive feedback to drain is shown in Fig. 4.

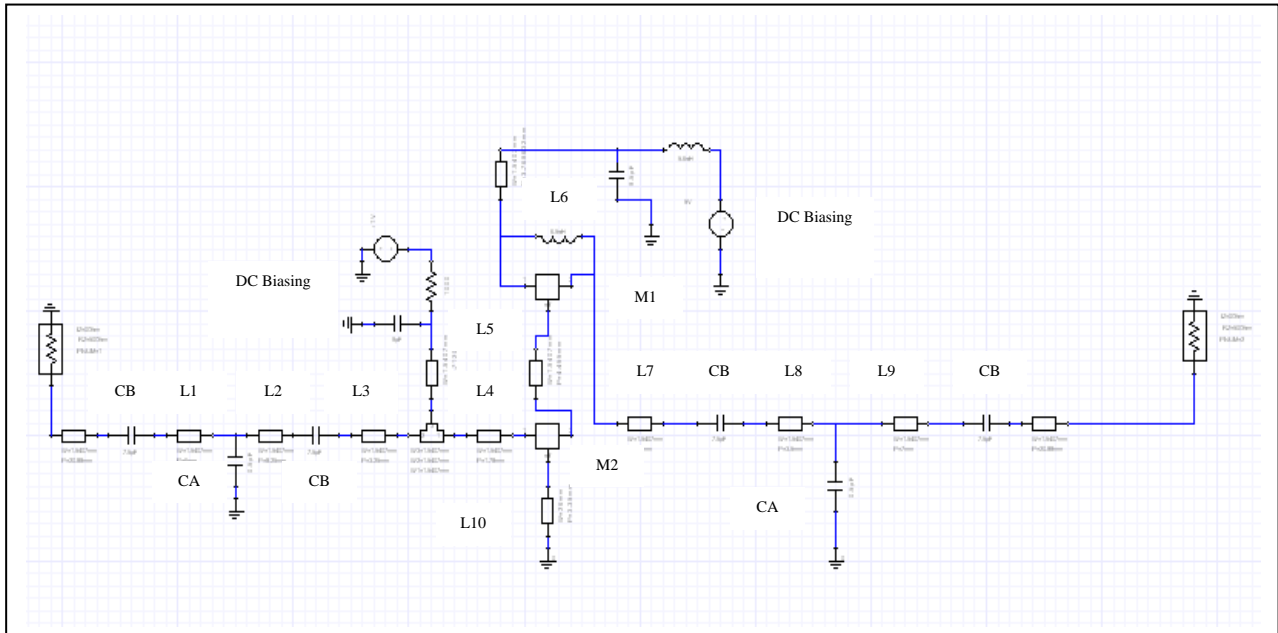


Fig. 4. Complete schematic of the LNA Cascoded amplifier circuit designed using inductive feedback to drain. From Fig. 4, inductive at the circuit has its own characteristics, for example, when performing the optimization in L6 we will obtain a high gain, but whenever the value of L6 exceeds 50% of the stated value it will cause the LNA amplifiers be unstable (potentially unstable) that is going to happen isolation and bandwidth will be reduced significantly. The degenerating inductor L10, which gives the LNA its purely real input impedance [5] and help in getting the input and output of the optimal matching, when this condition occurs bandwidth (BW) and noise figure (NF) will not decline further and set at a value. The cascoded transistor M2 suppresses the Miller capacitance of M1 thereby increases the reverse isolation. The suppression of the parasitic capacitances of the input transistor also improves the high frequency operation of the amplifier [5]. Between M2 and M1 are placed inductive L5 that will help in getting higher input impedance matching. When the L5 altered and elevated the value of K values also increased drastically, and that show cascoded LNA amplifiers are in unconditionally stable and will not isolate. Whenever L5 value increased by 50% from the value specified isolation may be seen that the value of  $K < 1$  and the value of gain and noise figure will decrease. Passive elements contained in the input matching network at the LNA circuit is built as follows L1, L2 and CA1. While there are passive elements in the output matching network, they are L7, L8, L9 and CA2. From the observations made at L1, L2, L3, L7, L8 and L9 after optimizing we obtained high input / output impedance that will affect the input and output matching to 50 Ohm terminal. This causes us to improve the value of S-parameters S11 and S22 according to the required specifications of  $< -10$ . Other than that it will also offer higher gain at the desired frequency and to control the bandwidth (BW) in cascoded LNA circuit. Capacitor CB is acting as a DC

block to cascaded LNA circuit built, in which they proposed is worth 10 times the original value of the CB because it acts as a bypass capacitor [11].

#### IV. SIMULATION OF RESULT

Result of simulations performed on the cascaded LNA circuit for the noise figure output observed in Fig 5 (a). is -0.83 dB. For this amplifier, the consideration is on the maximum gain with low noise figure less than 3 dB. This S-parameter output is acceptable with the targeted specification required for the system.

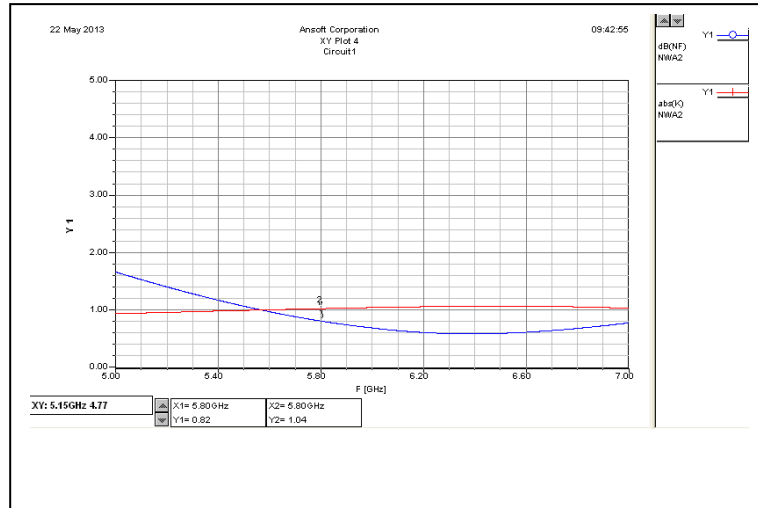


Fig 5 (a). Noise Figure and Stability factor after matched load

The stability factor after matching load is shown in Fig 5 (a), and the stability obtained is 1.04. These parameters are compliant with the targeted specifications of the amplifier for unconditional stable condition  $k > 1$  mean that no isolation occur at the amplifier. The noise figure output observed is 0.83dB.

The output gain and reflection loss is shown in Fig. 5 (c). Simulated S-parameters for the amplifier output shows that, the output gain  $S_{21}$  at 5.8 GHz is 26.26 dB and reflection loss  $S_{12}$  is -30.90 dB. While Fig 5 (d) refers to the input return loss  $S_{11}$  and  $S_{22}$  output return loss, each has value -11.10 dB and -10.50 dB respectively. From Fig. 15 (c), we measured that, the 3dB bandwidth obtained is 1.56 GHz compliant with targeted result of more than 1 GHz.

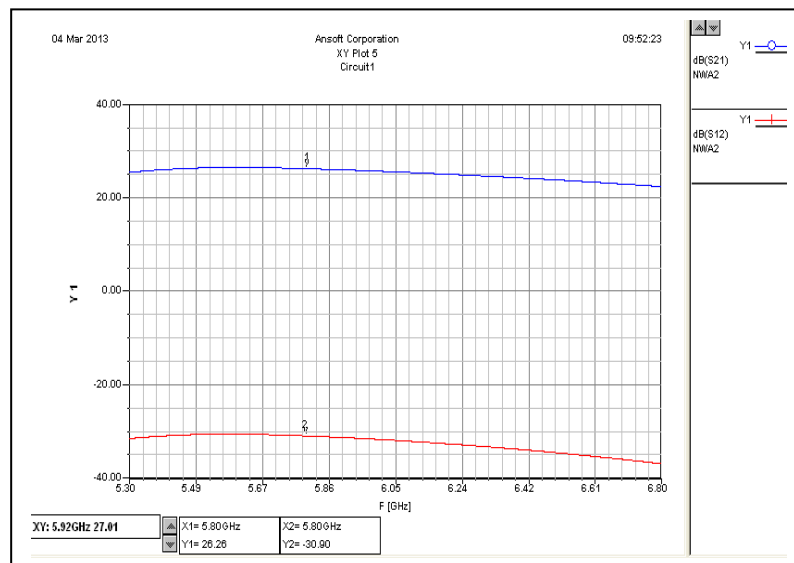


Fig. 5 (c) Output Gain ( $S_{21}$ ) and Reflection Loss ( $S_{12}$ )

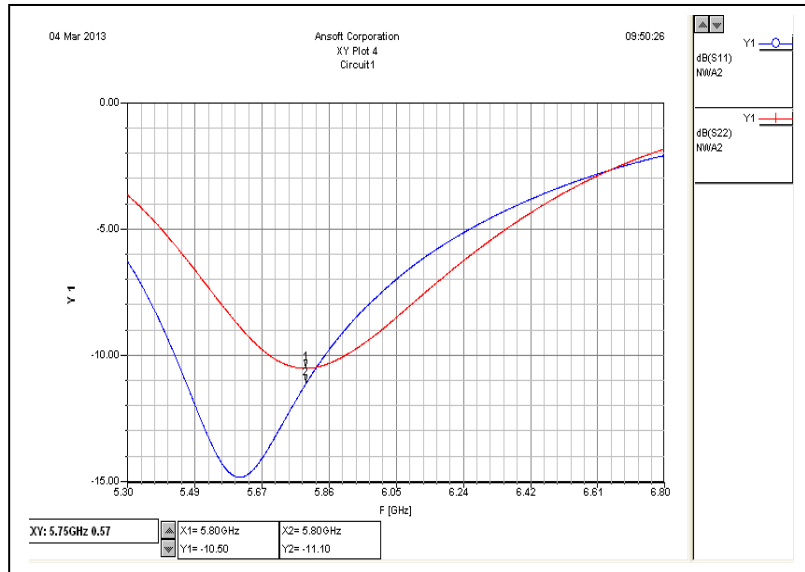


Fig 5 (d). Input Return Loss ( $S_{11}$ ) and Output Return Loss ( $S_{22}$ )

The simulated of S-parameters of the cascoded LNA amplifier as shown in Table 5.

Table 5: S-Parameter Output Targeted and Simulated Parameters of Single Cascoded LNA at 5.8 GHz

S-Parameter	Input reflection $S_{11}$ dB	Return Loss $S_{12}$ dB	Forward Transfer $S_{21}$ dB	Output Reflection loss $S_{22}$ dB	Noise Figure NF dB *	Bandwidth GHz	Stability K
Targeted Single Cascoded LNA	<-10	<-10	>20	<-10	<3	>1	>1
Simulated Single Cascoded LNA	-11.10	-30.90	26.26	-10.50	0.83	1.56	1.04

The comparison and lists of performances cascoded LNA is shown in Table 6.

Table 6. Comparison with recently Cascoded LNA

Author	Technology	Freq (GHz)	Gain ( $S_{21}$ ) dB	NF (dB)	$S_{11}$ (dB)	$S_{22}$ (dB)	Bandwidth (GHz)	Stability (K)
[13]	Super HEMT	5.8	19.5	1.2	-18.9	-19.49	> 1	1.016
[4]	0.18um CMOS	2-6	13.5	2.7-4.5	<-9	<-9	-	-
[5]	90nm CMOS	5.8	13.8	1.7	-	-	-	-
[12]	Gas pHEMT	5.5	11	1.6	-9.2	-8	-	-
<b>[This Work]</b>	Super HEMT	5.8	26.26	0.83	-11.1	-10.5	1.56	1.04

Note \*\*: (-) – not mention

### V. FUTURE WORK

The result from simulated parameter to fulfills overall specification targeted. However, there still have improvement can be made to improve overall performance. Therefore, it is recommended to design a double-

stage of LNA amplifier in order to exhibit a better gain with low noise figure. The proposed topology of the double-stage LNA can be seen in Fig. 6. With this newly proposed topology could exceed the speed of 70 Mbps with the coverage of 50km.

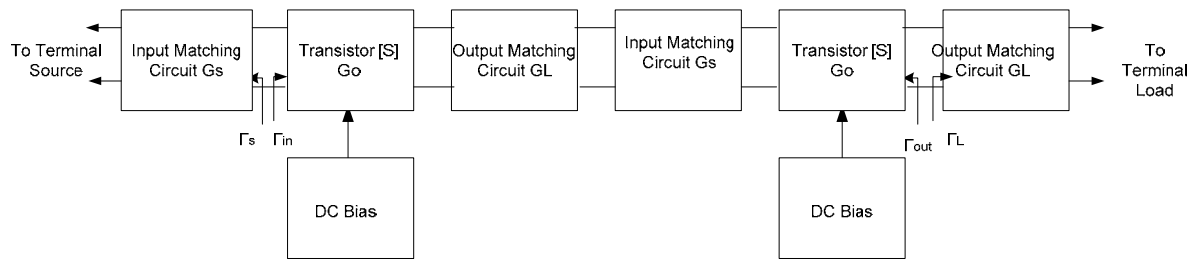


Fig. 6 The proposed double-stage LNA amplifier

## VI. CONCLUSIONS

The cascoded LNA amplifier with inductive drain feedback has been simulated and developed successfully according to IEEE standard 802.16 WiMAX. It is observed that from simulated S-parameter results the amplifier achieved the targeted specification shown in Table 5. The cascoded topology was chosen for this design as it offers an improvement in gain, low noise figure, reverses isolation and reduces the miller effect. The cascoded LNA designed amplifier achieved the lowest noise figure and high gain due to the noise optimization in the implementation of the input matching using inductive degeneration and used inductive drain feedback. At a frequency of 5.8 GHz cascoded LNA amplifiers recorded gain  $S_{21}$  of 26.26 dB. While input Insertion loss  $S_{11}$  is -11.1 dB and, the output insertion loss  $S_{22}$  is -10.50 dB. The  $S_{12}$  reflected loss is -30.90 dB. The stability (K) and noise figure (NF) was 1.04 and 0.83 respectively.

## VII. ACKNOWLEDGMENT

The work described in this paper was fully supported by Centre For Research And Innovation Management (CRIM), Universiti Teknikal Malaysia Melaka (UTeM), Melaka, Malaysia.

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