

Design and Implementation of Digital Notice Board Using Power Line Communication

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ABSTRACT:- Power line communication is one of the emergent technologies used in a cost effective and high speed communications of digital and voice data. It has been used successfully in many real time applications. The paper proposes one such application for automating an educational institution by replacing manual notice boards or circulars by digital notice boards. With a centralized database, frequent updating is easily possible. The system uses existing power lines to send the data to a specific node or to broadcast to various power line nodes. The address is assigned to each receiver and it response based on their appropriate commands. The information to be displayed is received from the main server and at reception each node checks the password and displays it. The design consists of a PC, PLC modem, controller kit and an LCD. The proposed model with a wired communication is better than the wireless communication system.

Keywords - PLC (Power Line Communication), PLC Modem, PIC 16F877A Microcontroller, PC (Personal Computer), LCD

I. INTRODUCTION

In educational institutions, the organization uses circulars and notice boards in order to convey information to the students. This methodology will take added time for updating and many students may not be aware of the information displayed on notice boards which are not eye catching ones. Power line communications (or PLC) overcomes the above said problems. It operates by sending high frequency signals over the power cable which already carries the electricity power. These signals can be transmitted to the power nodes under a substation unit but cannot pass through a transformer.

An NS-2 network simulator was developed for home networking based on power line communication. This process is used for end to end data communication at time varying packet error rate. Based on the PLC network condition, TCP-ACK rate was controlled [1][2]. The PLC was used in solar power system with a QPSK (Quadrature Phase Shift Keying) modems. The system had three-phase inverter based IGBT to create AC line three-phase voltages and maximum power point tracking system [3]. Hakki Cavdar et al. Presented an Automatic meter reading through Power Line Communications included in a power delivery module to detect illegal electricity usage. New digitally energy meter chip is used in the detection module and the charge of energy is stored[4]. The design of automatic meter reading in broadband carrier was introduced to overcome the drawbacks of narrowband communications. The relay routing algorithm was used to measure the rate of meter reading [5]. A home server which chains the user interface web page and PPCOM (PLC Power-Controlled Outlet Module) permits the user to effortlessly control the electric home appliances by the Internet is discussed by Chia-Hung Lien et al [6]. Microcontroller based master and slave units with serial ports of communication, to couple the modem signal with a transmission port and computer at both ends clearly explained by Sheroz Khan et al [7]. Hitoshi Kubota et al., analyzed a technique to avoid noise from home appliances and steep attenuation on frequency response of power line. Data rate transfer having selective frequency and appropriate modulation was used to realize the robust condition [8].

The noise over a power line communication was overcome by implementing a Wigner Ville distribution (WVD) with wavelet transform methods. WVD being a real energy preserving transform provides greater resolution in time and frequency. This system is a powerful investigation tool for signal readability and time frequency localization to detect if any power line signal contains noisy information [9]. To reduce the sum of ICI (Inter carrier Interference) and ISI (Inter symbol Interference) powers in Daubechies wavelet-based OFDM system, a wavelet packet structure was used over power line communication. Based on a brute-force algorithm the operation takes place [10]. In power line communication the possibility with time-domain synchronous orthogonal frequency division multiplexing (TDS-OFDM) technology was measured. In digital multimedia communications- terrestrial (DMB-T) the TDS-OFDM core technology was implemented in [11]. Rozeha A. Rashid et al. explored and developed a protocol linking the use of power line as a data transmission

medium. The designed protocol has features such as multimode simplex communication, flood transmission and even parity error detection [12]. A test bed was developed for power line communication to operate in the 230V/50Hz power line environment. The target frequency range was nearly 1MHz to 10MHz. This approach has high channel impedance controllability, through high frequency communication signal coupling [13].

II. PROPOSED SYSTEM

A new design is built by incorporating methodologies mentioned in existing systems for implementing digital notice board in educational institutions. The data is transmitted in a selective and secure manner through the power lines. The data to be displayed is entered into the host PC (Personal Computer) using VB6.0 (Visual Basic) software. The transmitter side PLC modem connected serially with the PC, processes the data by modulating with carrier signal. The modulated signal is transmitted through A.C power line and received by another PLC modem interfaced with a PIC controller. The received signal is demodulated and the controller displays the information on the LCD. Fig.1 shows the hardware configuration of the entire system.

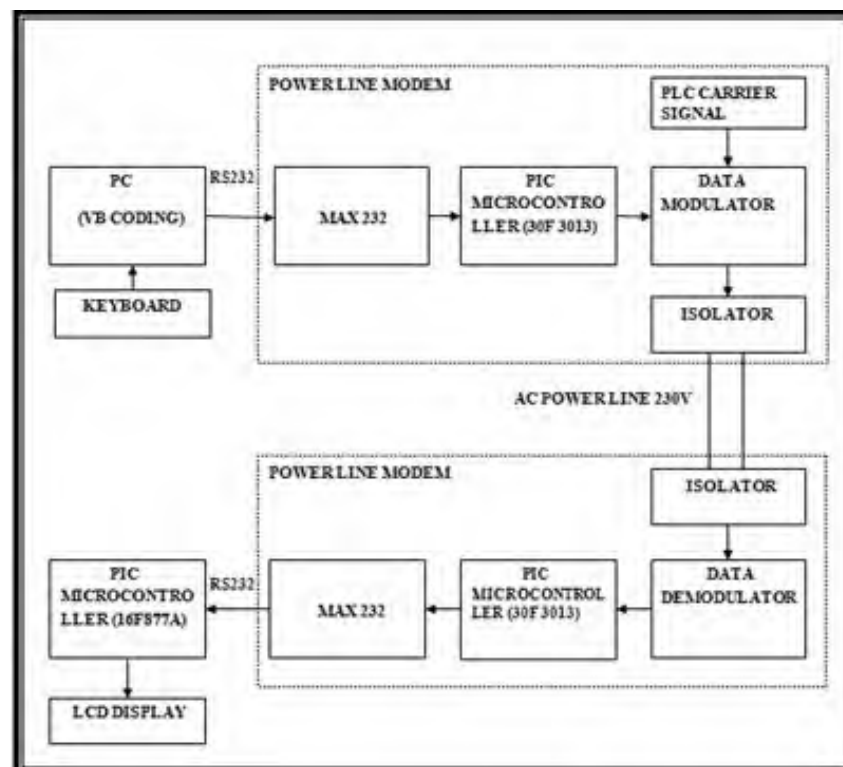


Fig.1. Entire block diagram of the proposed system

The software used for this application is installed on a PC. Input is given from it and it has the serial port connected to the transmitting PLC modem. Power line modem receives the message (character) through the serial port. Microcontroller (dsPIC30F3013) passes the message to the data modulator which uses FSK (Frequency Shift Keying) method. Signal is passed to isolator in order to protect it from over voltage and send through ac power line 230V and the receiver demodulates the carrier signal. The PIC 16F877A, which is a 8 bit, 40 pin IC having 5 ports is ideally used in low cost and low power applications [14]. This receives the decoded message and displays the character in LCD HD44780.

III. SOFTWARE DESCRIPTION

The code for PIC microcontroller is run using MPLAB IDE (Integrated Development Environment) in windows environment. To build in applications for Microchip microcontrollers and to create hex files, MPLAB IDE is used [15]. The PICKIT2 programmer is used to download a hex file in microcontroller chip [16]. Fig.2. shows the compilation screenshot.

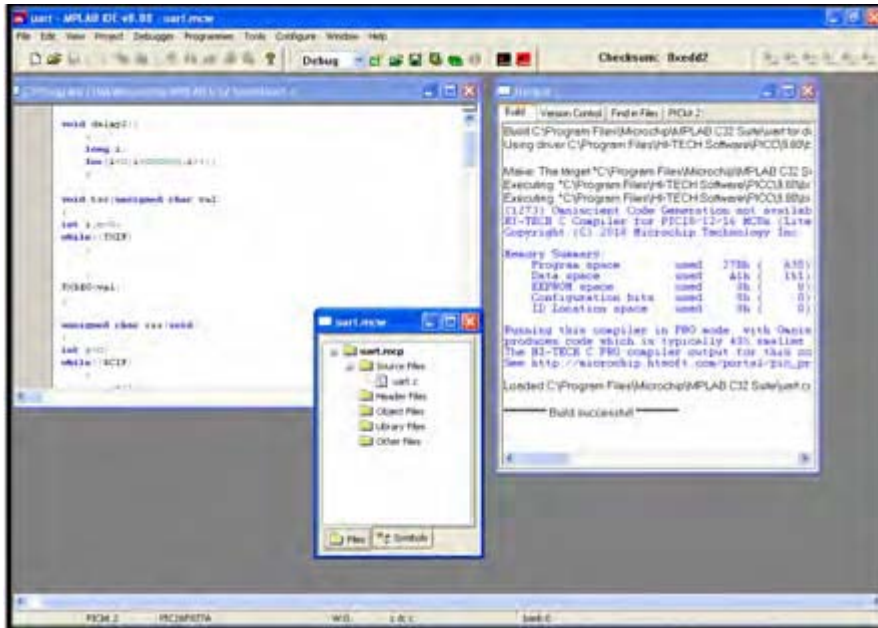


Fig.2. Compilation result in MPLAB

The form is designed using Microsoft Visual Basic 6.0 shown in Fig.3.

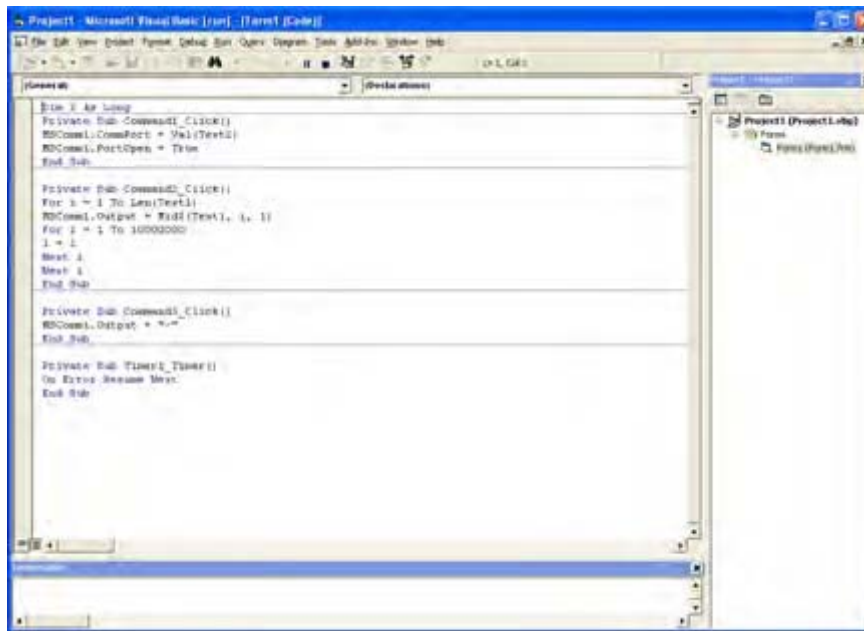


Fig.3.Creating Form in Microsoft Visual Basic

IV. RESULTS

Fig.4. shows the form design in visual basic 6.0 to give input message. Fig.5. shows the entire hardware setup of the reception unit consisting of PIC microcontroller and receiver PLC modem. Fig.6. shows the transmitter and receiver of PLC modem. Fig.7. displays the entered information in LCD.



Fig.4. Form design in Visual Basic

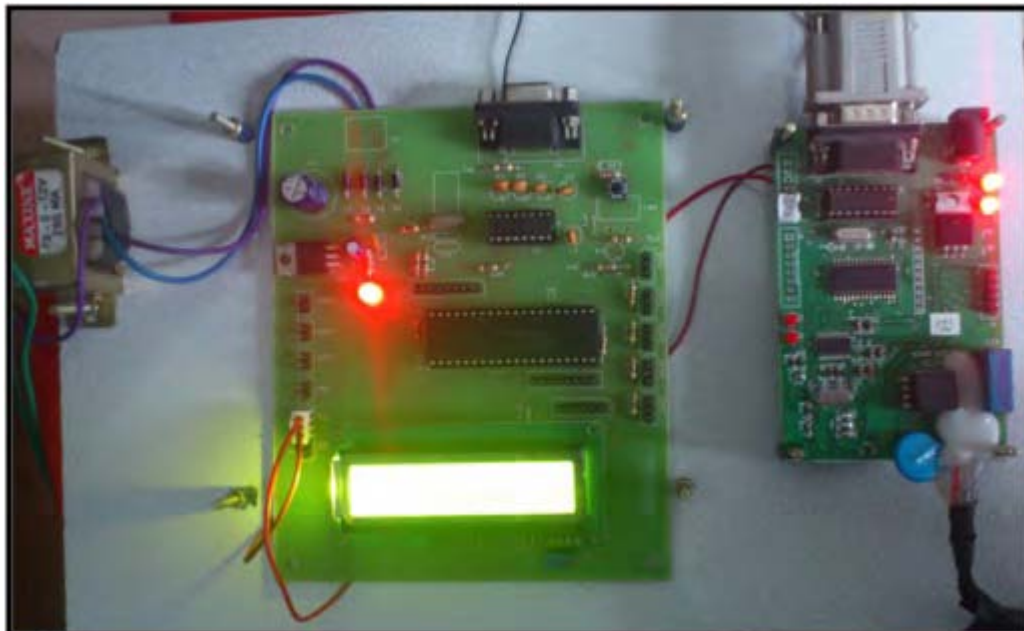


Fig.5. Hardware setup of the Receiver Unit

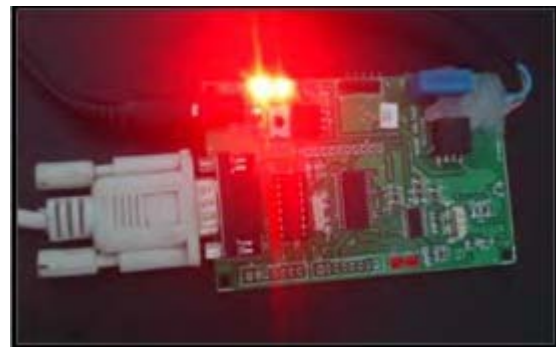
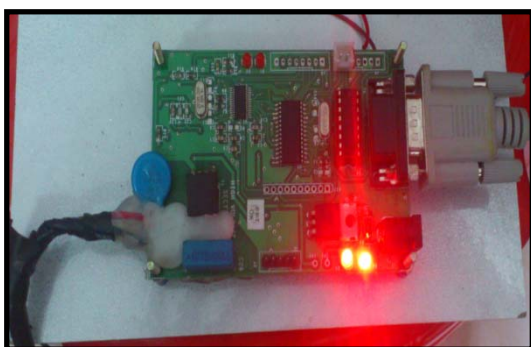


Fig.6. Power line transceiver modem



Fig.7. LCD Display shows the entered Information

V. CONCLUSION

The Power Line Communication (PLC) is used to deliver messages economically in educational institutions, offices, etc. The existing power line itself can be used to send/receive messages and it is highly real time oriented application. The centralized system is used to monitor the process with the help of VB software. It can be further enhanced by introducing two way communications to get acknowledgement from students.

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